

Personal information

First name(s) / Surname(s) **Dinu Coltuc**

E-mail(s) coltuc@valahia.ro

Date of birth 26 Oct 1958



Work experience

Dates 01/10/2000→

Occupation or position held Professor with the Electrical Engineering, Electronics and Information Technology Faculty, Director of the Doctoral School in Engineering of UVT (since nov. 2011), Director of the Research Centre for Electrical Engineering, Electronics and Information Technology (since 2009), Head of Electronics and Telecommunications Dept. (2002-nov. 2011)

Main activities and responsibilities Teaching: Image Processing, Signal Processing. Research in image processing, watermarking
Name of employer Valahia University Targoviste (Romania)

Dates 01/09/2009 – 30/11/2009, 01/10/2007 - 31/12/2007

Occupation or position held Invited Professor

Main activities and responsibilities Teaching: Image Segmentation and Analysis, Research in reversible watermarking
Name of employer Ecole Nationale Supérieure d'Ingénieurs pour l'Energie, l'Eau et l'Environnement & GIPSA-Lab (2009), Ecole Nationale Supérieure d'Ingénieurs Electriciens de Grenoble & LIS (2007), Grenoble INP

Dates 01/10/2005 - 31/08/2006

Occupation or position held Associate Researcher CNRS

Main activities and responsibilities Researches in Reversible Watermarking, Teaching Image and Signal Processing
Name of employer Laboratoire des Images et des Signaux – LIS, Grenoble (France)

Dates 01/02/2000 - 31/07/2000, 01/02/2001 - 31/07/2001, 01/02/2002 - 31/07/2002

Occupation or position held Invited Professor PAST (18 months)

Main activities and responsibilities Teaching: Image Processing, Signal Processing. Researches in Robust Watermarking
Name of employer Ecole Nationale Supérieure d'Ingénieurs Electriciens de Grenoble & LIS, INP Grenoble

Dates 01/09/1997 - 31/12/1997, 01/09/1998 - 31/12/1998, 01/09/1999 - 31/12/1999

Occupation or position held Invited Professor (12 months)

Main activities and responsibilities Teaching: Signal Processing and Image Processing. Research in fast algorithms, watermarking
Name of employer Ecole Supérieure d'Ingénieurs d'Annecy, Université de Savoie (France)

Dates 01/11/1993 - 31/01/1995

Occupation or position held Associate Researcher

Main activities and responsibilities Research in nonlinear filters, mathematical morphology, algorithms for signal/image processing
Name of employer School of Electrical and Computer Engineering, Aristotle University of Thessaloniki (Greece)

Dates 01/09/1984 - 30/09/2000
Occupation or position held Researcher (1984-1998), Senior Researcher (since 1998), Deputy (Feb. 1991 to March 1992, Feb. 1995 to Feb.1996) and Head (Apr. 1992 to Nov. 1993) of the Sensors and Transducers Dept.
Main activities and responsibilities Principal investigator in more than 10 research projects on Image Processing, Image Analysis, Pattern Recognition, Supervised Learning, Image Coding, Fast Algorithms, Image Algebra, etc.
Name and address of employer Research Institute for Electrical Engineering, Bucharest, Romania

Dates 01/09/1982 – 31/08/1997
Occupation or position held Associate Researcher with Image Process. LAB
Main activities and responsibilities Course and seminar on Image Processing (1991-1993, 1995). Participant (1984-1987), in the design and the manufacture of the first professional image processing system built in Romania (design of image memory, software).
Name and address of employer Faculty of Electronics and Telecommunications, University "Politehnica" of Bucharest (Romania)

Education and training

Dates 1977 - 1982
Title of qualification awarded Electronics Engineer (MSc)
Name of organisation Faculty of Electronics and Telecommunications, University "Politehnica" of Bucharest.

Dates 1995 - 1997
Title of qualification awarded PhD in Electronics and Telecommunications
Name of organisation Faculty of Electronics and Telecommunications, University "Politehnica" of Bucharest.

Personal skills and competences

Mother tongue(s) **Romanian**

Other language(s)

Self-assessment
European level (*)

English

French

Understanding				Speaking				Writing	
Listening		Reading		Spoken interaction		Spoken production			
C2	Proficient user	C2	Proficient user	C2	Proficient user	C1	Proficient user	C2	Proficient user
C2	Proficient user	C2	Proficient user	C2	Proficient user	C2	Proficient user	C1	Proficient user

(*) [Common European Framework of Reference \(CEF\) level](#)

Additional information

-Co-recipient of the Romanian Academy prize "Traian Vuia" for Contributions to the development of digital image processing systems (1987)
-Member of the National Council for Attestation of University Degrees, Diplomas and Certificates – CNATDCU (2016-2020)
-Associate Editor for IEEE Transactions on Information, Forensics and Security, (ISI Q1, since 2016) and Journal of Visual Communication and Image Representation (ISI Q2, since 2016).
-Member of the Evaluation Committees CES 27 (2014), CES39 (2016, 2017) of National Research Agency (ANR) France
-Senior IEEE Member (since 2011)
-PI in 14 national research projects. Participant in 6 national projects and 3 international projects
-Served as reviewer for IEEE T-IP, IEEE T-IFS, IEEE T-CSVT, IEEE T-MM, IEEE T-PAMI, IEEE T-SP, IEEE SPL, Journal on Syst. & Software, Signal Process., JVCI, DSP, MTAP, IET Image Process., etc.
-Reviewer for intl. conf. IEEE ICASSP, IEEE ICIP, EUSIPCO, IEEE WIFS, IEEE ICME, ISSCS, etc.
-Chairman IEEE ISSCS, (2007-2019), EUSIPCO'12, IHMMSeC'13, EUSIPCO'14, etc.

LIST OF PUBLICATIONS (SELECTION)

Journal papers

1. I. Caciula, H.G Coanda, D. Coltuc, Multiple Moduli Prediction Error Expansion Reversible Data Hiding, *Signal Processing: Image Communication*, vol.71, pp. 120-127, 2019.
2. D. Coltuc, M. Datcu, D. Coltuc, On the Use of Normalized Compression Distances for Image Similarity Detection, *Entropy*, 20(2), 99, doi:10.3390/e20020099, 2018.
3. I.-C. Dragoi, D. Coltuc, Adaptive Pairing Reversible Watermarking, *IEEE Transactions on Image Processing*, vol. 25, no. 5, p. 2420-2422, 2016.
4. I.-C. Dragoi, S. G. Stanciu, R. Hristu, H.-G. Coanda, D. E. Tranca, M. Popescu, D. Coltuc, Embedding Complementary Imaging Data in Laser Scanning Microscopy Micrographs by Reversible Watermarking, *Biomedical Optics Express*, Vol. 7, Issue 4, pp. 1127-1137, 2016.
5. I.-C. Dragoi, D. Coltuc, On Local Prediction Based Reversible Watermarking, accepted for publication in *IEEE Transactions on Image Processing*, vol. 24, no. 4, p. 1244–1246, 2015.
6. S. G. Stanciu, D. E. Tranca, D. Coltuc, Contrast Enhancement Influences the Detection of Gradient Based Local Invariant Features and the Matching of Their Descriptors, *Journal of Visual Communication and Image Representation*, 2015.
7. I.-C. Dragoi, D. Coltuc, Local Prediction Based Difference Expansion Reversible Watermarking, *IEEE Transactions on Image Processing*, vol. 23, no. 4, p. 1779-1790, 2014
8. D. Coltuc, Low Distortion Transform for Reversible Watermarking, *IEEE Transactions on Image Processing*, vol. 21, no. 1, p. 412-417, 2012.
9. D. Coltuc, Improved Embedding for Prediction Based Reversible Watermarking, *IEEE Transactions on Information Forensics and Security*, vol. 6, no. 3, p. 873-882, 2011.
10. S. G. Stanciu, G. A. Stanciu, D. Coltuc, Automated compensation of light attenuation in confocal microscopy by exact histogram specification, *Microscopy Research and Technique*, vol. 73, no. 3, 165-175, 2010.
11. D. Coltuc, Mathematical Complexity of Running Filters on Semi-Groups and Related Problems, *IEEE Trans. on Signal Processing*, Vol. 56, no. 7, p. 3191-3197, 2008.
12. D. Coltuc, J.-M. Chassery, Very Fast Watermarking by Reversible Contrast Mapping, *IEEE Signal Processing Letters*, vol. 15, nr. 4, p. 255-258, 2007.
13. D. Coltuc, Ph. Bolon, J.-M. Chassery, Exact Histogram Specification, *IEEE Trans. on Image Processing*, vol. 15, nr. 5, p. 1143-1152, 2006.
14. G. Chareyron, D. Coltuc, A. Tremeau, Watermarking and Authentication of Color Images Based on Segmentation of the xyz Color Space, *Journal of Imaging Science & Technology*, vol. 50, no.5, p. 411-423, 2006,
15. D. Coltuc, I. Pitas, Fast Computation of a Class of Running Filters, *IEEE Trans. on Signal Processing*, Vol. 46, nr. 3, p. 549-553, 1998.
16. D. Coltuc, I. Pitas, On Fast Running Max/Min Filtering, *IEEE Trans. on Circuits and Systems-II: Analog and Digital Signal Processing*, Vol. 44, nr. 8, p. 660-663, 1997.
17. D. Coltuc, I. Pitas, Morphological Residual Representations of Signals, *IEEE Trans. on Image Processing*, vol. 5, nr. 11, p. 1569-1572, 1996.
18. V. Buzuloiu, D. Coltuc, Small Image Processing Systems - Performances, Architectures, Functions, *Computer Physics Communications*, Vol. 50, p. 229-235, North Holland, Amsterdam, 1988.

Books & Books Chapters

1. D. Coltuc, Reversible Watermarking, in *Encyclopedia of Information Science and Technology*, Third Edition, Ed. Mehdi Khosrow-Pour, IGI Global, Hershey, Pennsylvania, pp. 7280-7288, 2015.
2. D. Coltuc, The use of watermarking in stereo imaging, in *Depth Map and 3D Imaging Applications: Algorithms and Technologies*, Ed. A. S. Malik, T.-S. Choi and H. Nisar, IGI Global, Hershey, Pennsylvania, p. 331-345, 2012.
3. S. G. Stanciu, G. A. Stanciu, D. Coltuc, Compensating light intensity attenuation in confocal scanning laser microscopy by histogram modeling methods, in *Digital Image Processing*, Ed. S. G. Stanciu, Intech, pp. 187-200, 2012.
4. D. Coltuc, *Algorithms for max/min filtering*, in Romanian, ICPE Publishing House, Bucharest, ISBN 973-8067-11-1, 2000.
5. D. Coltuc, *Fundamentals of Digital Image Processing*, in Romanian, ICPE Publishing House, Bucharest, ISBN 973-8067-04-9, 1999.
6. I. Pitas, D. Coltuc, Nonlinear Signal and Image Processing, in *Encyclopedia of Computer Science and Technology*, Vol. 37, Ed. A. Kent, J.G. Williams, Marcel Dekker, Inc., New York, p. 205-223, 1997.

International Conferences

1. I.C. Dragoi, D. Coltuc, "Prediction-error-ordering for High-fidelity Reversible Data Hiding," *IEEE International Conference on Acoustics, Speech and Signal Processing (ICASSP)*, Brighton, UK, 2019.
2. I.C. Dragoi, D. Coltuc, "Gradient Based Prediction for High Fidelity Reversible Data Hiding with Pairwise Embedding," *IEEE International Symposium on Signals, Circuits and Systems (ISSCS)*, Iasi, Romania, 2019.
3. A. Bobeica, I.C. Dragoi, I. Caciula, D. Coltuc, "Sample Value Ordering for Audio Reversible Data Hiding", *The 6th Intl. Symposium on Electrical and Electronics Engineering ISEEE*, 18 - 20 oct., Galați, Romania, 2019
4. I.C. Dragoi, D. Coltuc, "Reversible Data Hiding in Encrypted Color Images Based on Vacating Room After Encryption and Pixel Prediction," *25th IEEE International Conference on Image Processing (ICIP)*, Athens, Greece, 2018.

5. I.C. Dragoi, Ion Caciula, D. Coltuc, "Improved Pairwise Pixel-Value-Ordering for High-Fidelity Reversible Data Hiding," *25th IEEE International Conference on Image Processing (ICIP)*, Athens, Greece, 2018.
6. I.C. Dragoi, D. Coltuc, "Improved Pairwise Embedding for High-Fidelity Reversible Data Hiding," *European Signal Processing Conference EUSIPCO'2017*, Rome, Italy, 2018.
7. I.C. Dragoi, D. Coltuc, "Reversible Data Hiding in Encrypted Images Based on Reserving Room After Encryption and Multiple Predictors," *IEEE International Conference on Acoustics, Speech and Signal Processing (ICASSP)*, Calgary, Canada, 2018.
8. I.-C. Dragoi, H.-G. Coanda, D. Coltuc, Improved Reversible Data Hiding in Encrypted Images Based on Reserving Room After Encryption and Pixel Prediction, *European Signal Processing Conference EUSIPCO'2017*, Kos, Greece, 2017.
9. I.-C. Dragoi, D. Coltuc, H.-G. Coanda, Adaptive pairwise reversible watermarking with horizontal grouping, *Signals, Circuits and Systems (ISSCS)*, 2017 International Symposium on. IEEE, Iasi, Romania, 2017.
10. I.-C. Dragoi, D. Coltuc, Towards Overflow/Underflow Free PEE Reversible Watermarking, *European Signal Processing Conference EUSIPCO'2016*, Budapest, Hungary, p. 953-957, 2016.
11. I.-C. Dragoi, D. Coltuc, Reversible Watermarking Based on Complementary Predictors and Context Embedding, *European Signal Processing Conference EUSIPCO'2016*, Budapest, Hungary, p. 1178-1182, 2016.
12. I.-C. Dragoi, D. Coltuc, I. Caciula, Horizontal Pairwise Reversible Watermarking, *European Signal Processing Conference EUSIPCO'2015*, Nice, France, 2015.
13. T. Nedelcu, R. Iordache, D. Coltuc, Alternate multibit embedding method for reversible watermarking, *European Signal Processing Conference EUSIPCO'2015*, Nice, France, 2015
14. C. Dragoi, S. Stanciu, D. Coltuc, D. Tranca, R. Hristu, G. Stanciu, On Packing Laser Scanning Microscopy Images by Reversible Watermarking: a Case Study, *European Signal Processing Conference EUSIPCO'2015*, Nice, France, 2015.
15. I.-C. Dragoi, D. Coltuc, "A simple four-stages reversible watermarking scheme." *Signals, Circuits and Systems (ISSCS)*, 2015 International Symposium on. IEEE, Iasi, Romania, 2015.
16. I. Udroi, D. Coltuc. "On optimized histogram bin shifting reversible watermarking for color images." *Signals, Circuits and Systems (ISSCS)*, 2015 International Symposium on. IEEE, Iasi, Romania, 2015.
17. T. Nedelcu, D. Coltuc. "Alternate embedding method for difference expansion reversible watermarking." *Signals, Circuits and Systems (ISSCS)*, 2015 International Symposium on. IEEE, Iasi, Romania, 2015.
18. I.-C. Dragoi, D. Coltuc, I. Caciula, Gradient Based Prediction for Reversible Watermarking by Difference Expansion, *Proceedings of the second ACM workshop on Information hiding and multimedia security ACM IH&MMSec'14*, p. 35-40, Salzburg, Austria, 2014.
19. I. Caciula, D. Coltuc, Improved Control for Low Bit-Rate Reversible Watermarking, *IEEE International Conference on Acoustics, Speech, and Signal Processing (ICASSP)*, p. 7425 – 7429, Florence, Italy, 2014.
20. I.-C. Dragoi, D. Coltuc, "Textures and reversible watermarking." *Signal Processing Conference (EUSIPCO)*, 2014 *Proceedings of the 22nd European*, p. 2450-2454, Lisbon, Portugal, 2014.
21. T. Nedelcu, R. Iordache, D. Coltuc, "Three stages prediction-error expansion reversible watermarking," *Signal Processing Conference (EUSIPCO)*, *Proceedings of the 22nd European*. IEEE, p. 2455-2459, Lisbon, Portugal, 2014.
22. D. Coltuc, I.-C. Dragoi, Context embedding for raster-scan rhombus based reversible watermarking. *Proceedings of the first ACM workshop on Information hiding and multimedia security ACM IH&MMSec'13*, 10-12 June, p. 215-220, Montpellier, France, 2013.
23. I. Caciula, D. Coltuc, Capacity Control Of Reversible Watermarking By Two-Thresholds Embedding: Further Results, *IEEE International Symposium on Signals, Circuits and Systems, ISSCS'13*, Iasi, Romania, 2013.
24. I. Tudoroiu, D. Coltuc, Local Map Versus Histogram Shifting For Prediction Error Expansion Reversible Watermarking, *IEEE International Symposium on Signals, Circuits and Systems, ISSCS'13*, Iasi, Romania, 11-12 July, 2013.
25. F. Albu, D. Coltuc, K. Nishikawa, M. Rotaru, An efficient implementation of the kernel affine projection algorithm, *International Symposium on Image and Signal Processing and Analysis ISPA'2013*, September 2013.
26. I. Caciula, D. Coltuc, Capacity control of reversible watermarking by two-thresholds embedding, *Information Forensics and Security (WIFS)*, 2012 *IEEE International Workshop on*. IEEE, Tenerife, Spain, 2012.
27. D. Coltuc, A. Tudoroiu, Multibit Versus Multilevel Embedding in High Capacity Difference Expansion Reversible Watermarking, *European Signal Processing Conference EUSIPCO'2012*, Bucharest, August 2012.
28. C. Dragoi, D. Coltuc, Improved rhombus interpolation for reversible watermarking by difference expansion, *European Signal Processing Conference EUSIPCO'2012*, Bucharest, August 2012.
29. A. Tudoroiu, I. Caciula, D. Coltuc, Block Map Implementation of Difference Expansion Reversible Watermarking, *IEEE International Symposium on Signals, Circuits and Systems, ISSCS'11*, Iasi, Romania.
30. D. Coltuc, I. Caciula, H. Coanda, Color Stereo Embedding by Reversible Watermarking, *The Third International Symposium on Electrical and Electronics Engineering, ISEE-2010*, Galati, Romania, p. 256-259, 2010.
31. S. G. Stanciu, D. Coltuc, R. Hristu, C. Stoichita, G. A. Stanciu, Image Fusion for Photonic Quantum Ring Laser Structures Investigated by Confocal Scanning Laser Microscopy, *Intl. Conf. on Transparent Optical Networks - Mediterranean Winter 2009, ICTON-MV'09*, Angers, France 10-12 dec., 2009.
32. D. Coltuc, Modified Versions of Tian's Difference Expansion Reversible Watermarking, *Proceedings of the IEEE International Conference on Image Processing ICIP'09*, Cairo, Egypt, 2009.
33. D. Coltuc, I. Caciula, On Stereo Embedding by Reversible Watermarking: Further Results, *International Symposium on Signals, Circuits and Systems, ISSCS'09*, Iasi, vol. I, p. 121-124, 2009
34. D. Coltuc, Improved Capacity Reversible Watermarking, *IEEE International Conference on Image Processing ICIP'07*, San Antonio, SUA, vol. III, p. 249-252, 2007.
35. D. Coltuc, On Stereo Embedding by Reversible Watermarking, *IEEE International Symposium on Signals, Circuits and Systems ISSCS'07*, Iasi, p. 93-96, 2007.

36. D. Coltuc, J.-M. Chassery, Distortion-Free Robust Watermarking: a Case Study, *SPIE Proceedings, Security, Steganography and Watermarking of Multimedia Contents - VIII*, Editor(s): Eduard J. Delp III, Ping W. Wong, p. 8, vol. 6505, 2007.
37. D. Coltuc, Towards Distortion-Free Robust Image Authentication, *Journal of Physics: Conference Series*, IOP Publishing, vol. 77, p. 1-7, 2007.
38. D. Coltuc, J.-M. Chassery, High Capacity Reversible Watermarking, *Proceedings of the IEEE International Conference on Image Processing ICIP'06*, p. 2565-2568, Atlanta, GA SUA, 2006.
39. D. Coltuc, J.-M. Chassery, Simple Reversible Watermarking Schemes: Further Results, *SPIE Proceedings, Security, Steganography and Watermarking of Multimedia Contents - VIII*, Editor(s): Eduard J. Delp III, Ping W. Wong, vol. 6072, p. 739-746, 2006.
40. D. Coltuc, A. Tremeau, Simple Reversible Watermarking Schemes, *SPIE Proceedings, Security, Steganography and Watermarking of Multimedia Contents - VII*, Editor(s): Eduard J. Delp III, Ping W. Wong, vol. 5681, pp. 561-568, 2005.
41. D. Coltuc, A. Tremeau, Steganographie d'images basees sur le theoreme chinois des restes", *GRETSI'05*, pp. 1197-1200, Louvain-la Neuve, Belgique, 2005.
42. Chareyron, D. Coltuc, A. Tremeau, "Steganographie d'images couleur dans l'espace $L^*a^*b^*$ par modulation de distance, *GRETSI'05*, pp. 1037-1040, Louvain-la Neuve, Belgique, 2005
43. D. Coltuc, J.-M. Chassery, Ph. Bolon, Lossless Watermarking and Authentication of Color Images: a Case Study, *Proceedings of ACIVS'03 - Intl. Conf. on Advanced Concepts for Intelligent Vision Systems*, CD ROM Proceedings, p. 5, Ghent, Belgium, 2003.
44. D. Coltuc, Ph. Bolon, J.-M. Chassery, Towards Blind Detection in Regional Watermarking by Exact Histogram Specification, *Proceedings of ACIVS'02 - Intl. Conf. on Advanced Concepts for Intelligent Vision Systems*, CD ROM Proceedings, p. 5, Ghent, Belgium. September 9-11, 2002.
45. D. Coltuc, Ph. Bolon, J.-M. Chassery, Fragile and Robust Watermarking by Histogram Specification, *SPIE Proceedings, vol. 4657, Security and Watermarking of Multimedia Contents IV*, Editor(s): Delp, Edward J.; Wong, Ping W. USA, p. 701-710, 2002.
46. D. Coltuc, J.-M. Chassery, Ph. Bolon, Image Authentication by Exact Histogram Specification, *Proceedings of the IEEE Fourth Workshop on Multimedia Signal Processing, MMSP'01*, 3-5 Oct, Cannes, France, pp. 499-504, 2001.
47. D. Coltuc, Ph. Bolon, Color Image Watermarking in HSI Space, in *Proceedings of the IEEE International Conference on Image Processing ICIP'2000*, Vancouver, Canada, p. 687-701, oct. 2000.
48. D. Coltuc, Ph. Bolon, Fast Computation of Rank Order Statistics, *Proceedings of the European Signal Processing Conference EUSIPCO'2000*, Tampere, Finland, vol. IV, 2425-2428, 2000.
49. D. Coltuc, Ph. Bolon, Regional Watermarking in Spatial Domain, *Proceedings of the European Signal Processing Conference EUSIPCO'2000*, Tampere, Finland, vol. IV, p. 2165-2168, 2000.
50. D. Coltuc, Ph. Bolon, Robust Watermarking by Histogram Specification, *Proceedings of the IEEE International Conference on Image Processing, ICIP'99*, oct. 25-28, Kobe, Japonia, Vol. II, p. 236-239, 1999.
51. D. Coltuc, Ph. Bolon, Strict Ordering on Discrete Images and Applications, *Proceedings of the IEEE International Conference on Image Processing, ICIP'99*, oct. 25-28, Kobe, Japonia, Vol. III, p. 150-153, 1999.
52. D. Coltuc, Ph. Bolon, Very Efficient Implementation of Max/Min Filters, *Proceedings of the IEEE--EURASIP Workshop on Nonlinear Signal and Image Processing*, NSIP'99, Antalya, Turcia, Vol. II, p. 520-523, 1999.
53. D. Coltuc, Ph. Bolon, Watermarking by Histogram Specification, *SPIE Conference on Security and Watermarking of Multimedia Contents, SPIE Proceedings*, Vol. 3657, p. 252-263, San Diego, CA, USA, 1999.
54. J. M. Becker, D. Coltuc, Sandwich Distances: New Results, *SPIE Proceedings - Vision Geometry VII*, vol. 3454, p. 236-247, 1998.
55. D. Coltuc, Ph. Bolon, Decomposition and Order Statistics in Filtering, *Signal Processing IX, Theories and Applications, EUSIPCO'98*, Rhodos, Grecia, Vol II, p.813-816, 1998.
56. D. Coltuc, Ph. Bolon, An Inverse problem: Histogram Equalization, *Signal Processing IX, Theories and Applications, EUSIPCO'98*, Rhodos, Grecia, Vol II, p. 861-864, 1998.
57. J. M. Becker, D. Coltuc, M. Jourlin, Sandwich Distances, *SPIE Proceedings - Vision Geometry VI*, Vol. 3168, p. 22-32, 1997 & SPIE Vision Geometry, CD ROM 2000.
58. D. Coltuc, J. M. Becker, Scan-Directional Architectures, *Intl. Conf. on Vector and Parallel Processing VECPAR96*, Porto, Portugalia, p. 7.1-7.12, 1996. si Lecture Notes in Computer Science, Vector and Parallel Processing VECPAR96 - Selected Papers, Ed. J. Palma, J. Dongarra, Vol. LNCS 1215, Springer Verlag, p. 35-49, 1997.
59. D. Coltuc, Optimal Algorithms for Max/Min Filtering, *Proceedings of IEEE Intl. Symposium on Circuits and Systems, ISCAS'96*, Vol III, p. 129-133, Atlanta, USA, 1996.
60. D. Coltuc, J. M. Becker, V. Buzuloiu, Jordan Features for Image Segmentation, *Proceedings of the 3rd IEEE International Conference on Electronics, Circuits and Systems ICECS'96*, Rhodos, Grecia, Vol I, p. 195-198, oct. 1996.
61. D. Coltuc, I. Brulea, V. Buzuloiu, A Very Fast Algorithm for Min/Max Filtering, *Proceedings of the 3rd IEEE International Conference on Electronics, Circuits and Systems ICECS'96*, Rhodos, Grecia, Vol I, p. 464-466, oct. 1996.
62. D. Coltuc, I. Pitas, Jordan Decomposition Filters, *Proceedings of the IEEE International Symposium on Circuits and Systems, ISCAS'95*, Seattle, Washington, aprilie 29 - mai 5, Vol 3, p. 1896--1899, 1995.
63. D. Coltuc, I. Pitas, Morphological Signal Decomposition, *IEEE Workshop on Nonlinear Signal and Image Processing*, Neos Marmaras, Grecia, Vol. II, p. 983-986, 1995.
64. D. Coltuc, I. Pitas, 1D and 2D Jordan Decomposition Filters, *IEEE Workshop on Nonlinear Signal and Image Processing*, Neos Marmaras, Grecia, iunie 20-22, Vol. II, p. 979-982, 1995.
65. D. Coltuc, I. Pitas, Fast Running Max/Min Filters, *IEEE Workshop on Nonlinear Signal and Image Processing*, Neos Marmaras, Grecia, Vol. II, p. 871-873, 1995.
66. D. Coltuc, Curves Generation on Discrete Lattices, *SPIE Conference on Vision Geometry IV*, San Diego, California, iulie 1995, SPIE Proceedings, Vol. 2573, p. 137-147, 1995.

67. D. Coltuc, I. Pitas, Low Level Image Processing by Jordan Decomposition Filters, *ECCTD'95 - European Conference on Circuit Theory and Design*, 27-31 august, Istanbul, p. 817-820, 1995.
68. D. Coltuc, I. Pitas, Memory Mappings for Fast Peano Scan, *ECCTD'95 - European Conference on Circuit Theory and Design*, Istanbul, Turcia, p. 1019-1022, 1995.
69. D. Coltuc, V. Buzuloiu, Mapping Rules for Multiple Fast-Scan-Patterns Memory Modules, *SPIE Proceedings of the 4th Conference on Optics, ROMOPTO'94*, Vol. 2541, p. 285-288, 1995.
70. D. Coltuc, I. Pitas, Morphological Signal Decomposition in Digital Signal Processing, *International Symposium on Mathematical Morphology - Poster Contributions, Fointanbleau*, p. 5-6, 1994.
71. V. Buzuloiu, D. Coltuc, Non-Linear Processing of Signals for Fast Feature Extraction, Further Results, *IEEE Winter Workshop on Nonlinear Digital Signal Processing*, Tampere, Finlanda, p. 6.3.71-6.3.76, 1993.
72. T. W. Rauber, D. Coltuc, A. S. Steiger-Garcia, Multivariate Discretization of Continuous Attributes for Machine Learning, *Proceedings of the 7th Intl. Symposium on Metodologies for Intelligent Systems*, Norway, K. S. Harber (Ed.), p. 80-94, Oak Ridge, TN, USA, 1993.
73. V. Buzuloiu, D. Coltuc, Fast Nonlinear Algorithms for Super Resolution in Geometric Features Extraction, *ECCTD' 93 - Circuit Theory and Design*, Davos, Elvetia, H. Dedieu (Ed.), Elsevier Science Publishers B.V., p. 1441-1446, 1993

Patents

1. D. Coltuc, M. Datcu, A. Gheorghe, R. Oprea, A. Zamfir, 2-Dimensional memory module, Patent OSIM 89116, 1984.
2. V. Buzuloiu, D. Coltuc, M. Datcu, A. Gheorghe, R. Oprea, A. Zamfir, Multiport reconfigurable memory module, Brevet OSIM 89117, 1984.