

# CURRICULUM VITAE

**Bogdan DUMITRESCU**  
**Professor, University Politehnica of Bucharest**

Complete name: Dumitrescu Bogdan-Alexandru  
Date/place of birth: May 7,1962, Bucharest, Romania  
Civil status: married, 2 (grown-up) children  
Citizenship: Romanian

## 1. EDUCATION:

1993: Ph.D. degree at the University Politehnica of Bucharest (UPB). Thesis title: “Parallel computation systems and algorithms”.

1987: M.Sc. degree at the Department of Automatic Control and Computers of the UPB.

Spoken languages: English, French, Romanian (native).

## 2. POSITIONS:

1.10.2003 – present: professor at the Department of Automatic Control and Computers, UPB.

1998 – 2015: part-time activity as senior researcher at Tampere International Center for Signal Processing (TICSP), Tampere University of Technology (TUT), Finland.

1.01.2010 – 31.12.2013: FiDiPro fellow, TUT, Department of Signal Processing.

1.10.1990 – 1.10.2003: various teaching positions at the Department of Automatic Control and Computers, UPB.

1992 – 1996: three stages as researcher at Universite Polytechnique de Grenoble, France, for a total duration of about 15 months.

1.10.1987–1.10.1990: software engineer at FEPER Bucharest (Peripheral Equipments Factory).

## 3. ACTIVITY:

At PUB I currently teach Numerical Methods (2<sup>nd</sup> year) and Scientific Writing (master) and I taught several other courses like Parallel Algorithms, Signal Processing, Scientific Computation, Advanced Signal Processing, Convex Optimization, Control Systems. I am a PhD advisor since 2007: four PhD students have graduated, two are currently active.

At TUT I did mostly research, oriented on convex optimization and its applications in signal processing. A part of this research has been done in cooperation with Nokia Research Center (3

patents). I was a FiDiPro fellow for the period 2010-2013, project “Convex optimization in audio and image processing”, granted by Tekes.

Main research interest: numerical methods and optimization in signal processing, especially using polynomials. Since 2010, I focused on sparse representations and related problems like dictionary learning, with emphasis on greedy methods.

Other activities:

- Associate editor (2008-2012) and area editor (2010-2014) at IEEE Transactions on Signal Processing.
- Associate editor (2015-) at Mathematical Problems in Engineering (Hindawi)
- Associate editor (2018-) at Algorithms (MDPI)
- Reviewer for more than 20 journals (*IEEE Trans. Signal Processing, IEEE Signal Processing Letters, IEEE Trans. Circuits and Systems, Signal Processing, Automatica, etc.*).
- Technical Program Committee member at EUSIPCO since 2007 and at other signal processing conferences, like IEEE Statistical Signal Proc. Workshop (2018, 2016), ICUWB 2015, Globecom 2014, CISP-BMEI (since 2012).

### **Publications:**

- More than 50 journal articles, some in top 25% based on current AIS: IEEE Trans. on Signal Processing (9 articles), IEEE Signal Processing Letters (10), Signal Processing (8), IEEE Trans. on Circuits and Systems I (1), Computers Graphics Forum (1), Optimization and Engineering (1), IEEE J. Selected Topics in Signal Processing (1)
- 90 conference papers
- 3 international patents (US9640187, US9076437, CN 102576538 B)
- 5 books (3 in Romanian, 2 in English), including the monograph *Positive Trigonometric Polynomials and Signal Processing Applications*, Springer, 2007.
- 2 book chapters
- complete list at [http://www.schur.pub.ro/BD\\_PublicationList.html](http://www.schur.pub.ro/BD_PublicationList.html)

Hirsch index: 13 ISI, 14 Scopus, 19 Google scholar

Citations: 550 WoS, 700 Scopus, 1200 Google scholar (Nov. 2018)

Grants and contracts:

- Sparse representations and signal processing applications, IDEI 2011-2016
- Positivity in the analysis and synthesis of multidimensional systems, IDEI 2007-2010
- Several contracts for filter bank design with Nokia Research Center and Microsoft Finland